

Fig. 1

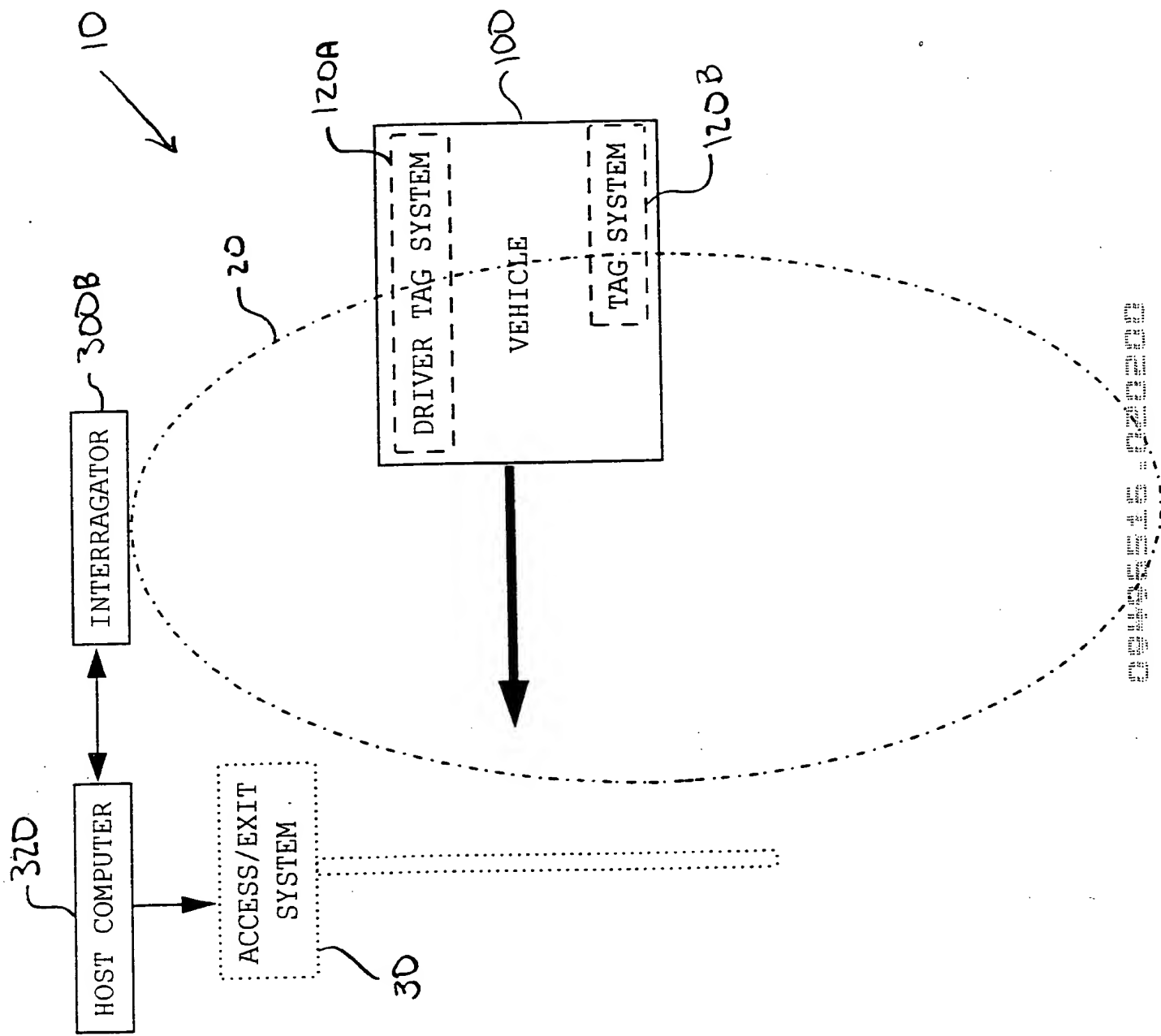


Fig. 1A

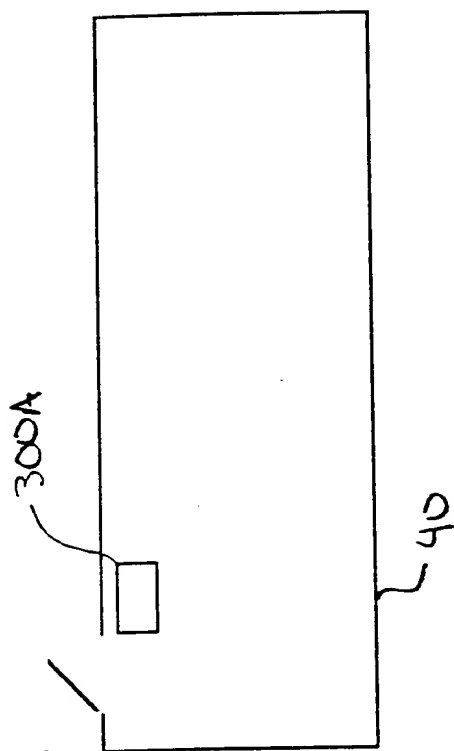


FIG. 1A

Fig. 2

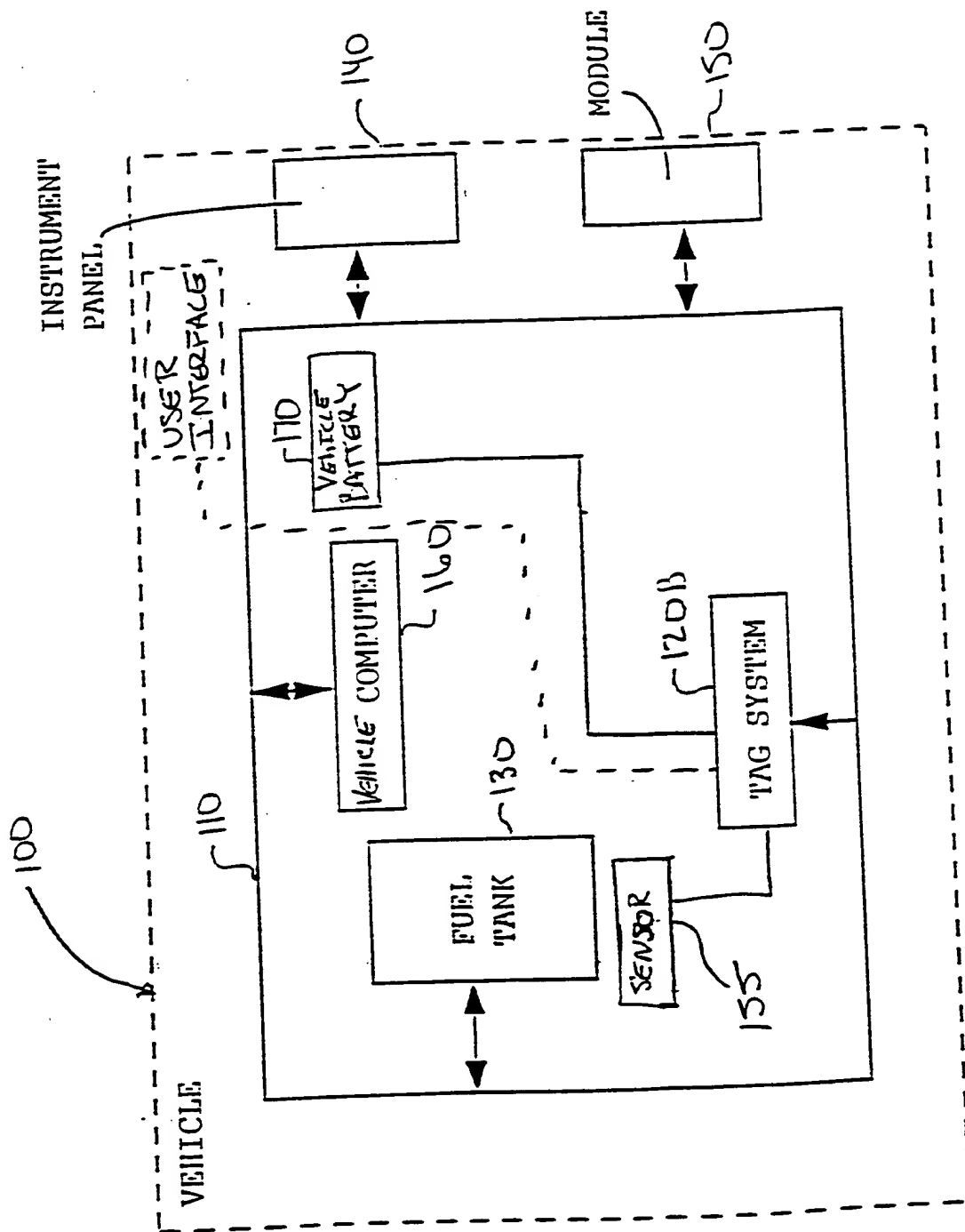
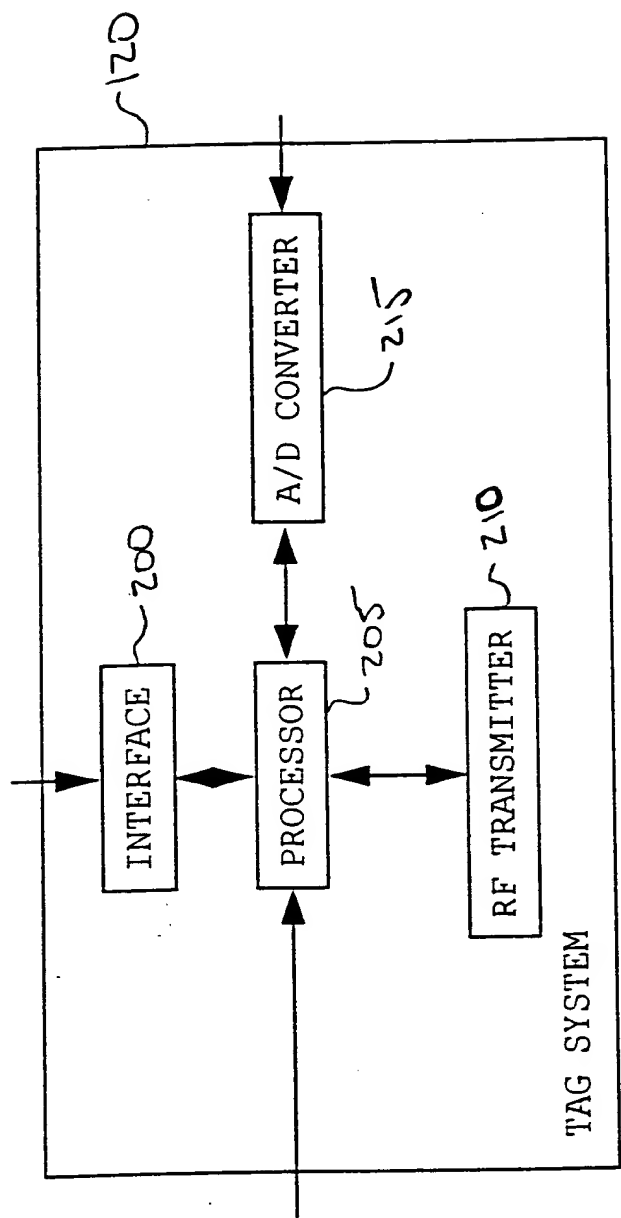


Fig. 3A



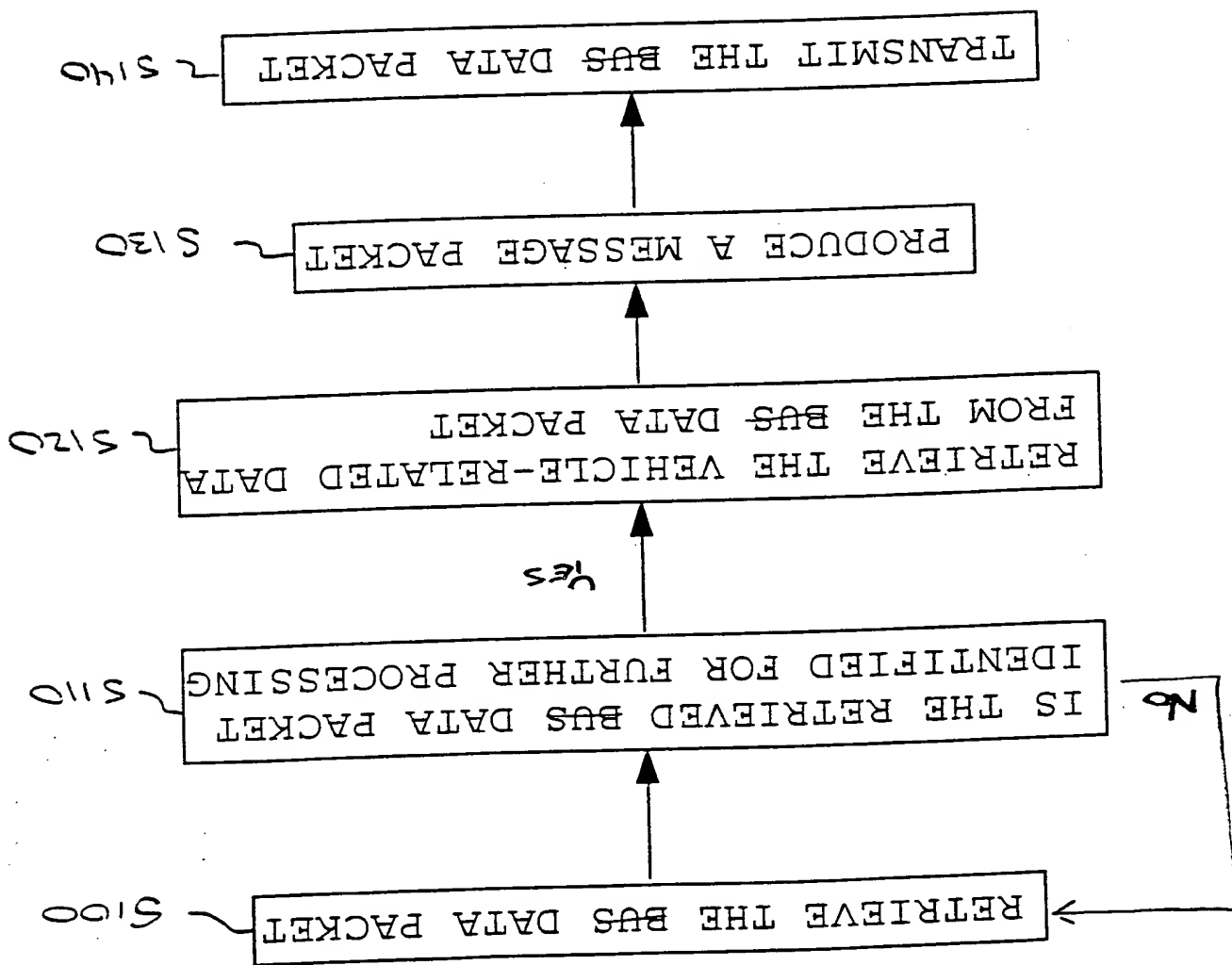


Fig. 38

Fig. 4

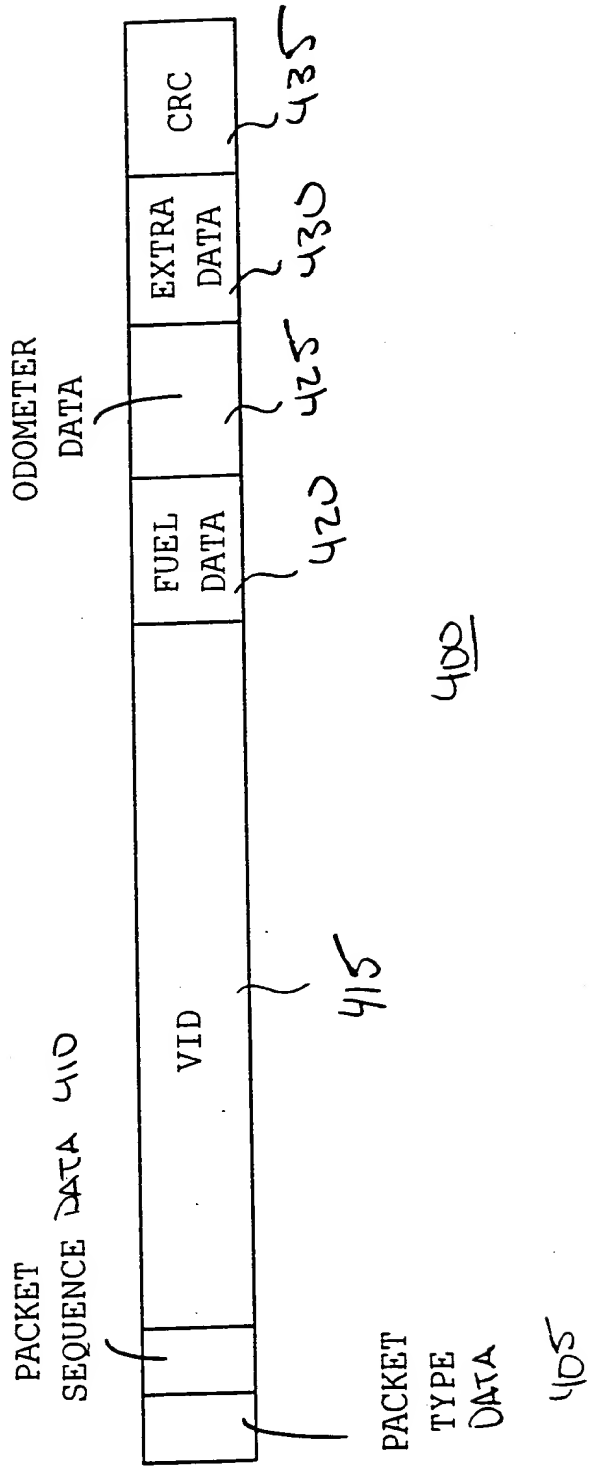


Fig. 5

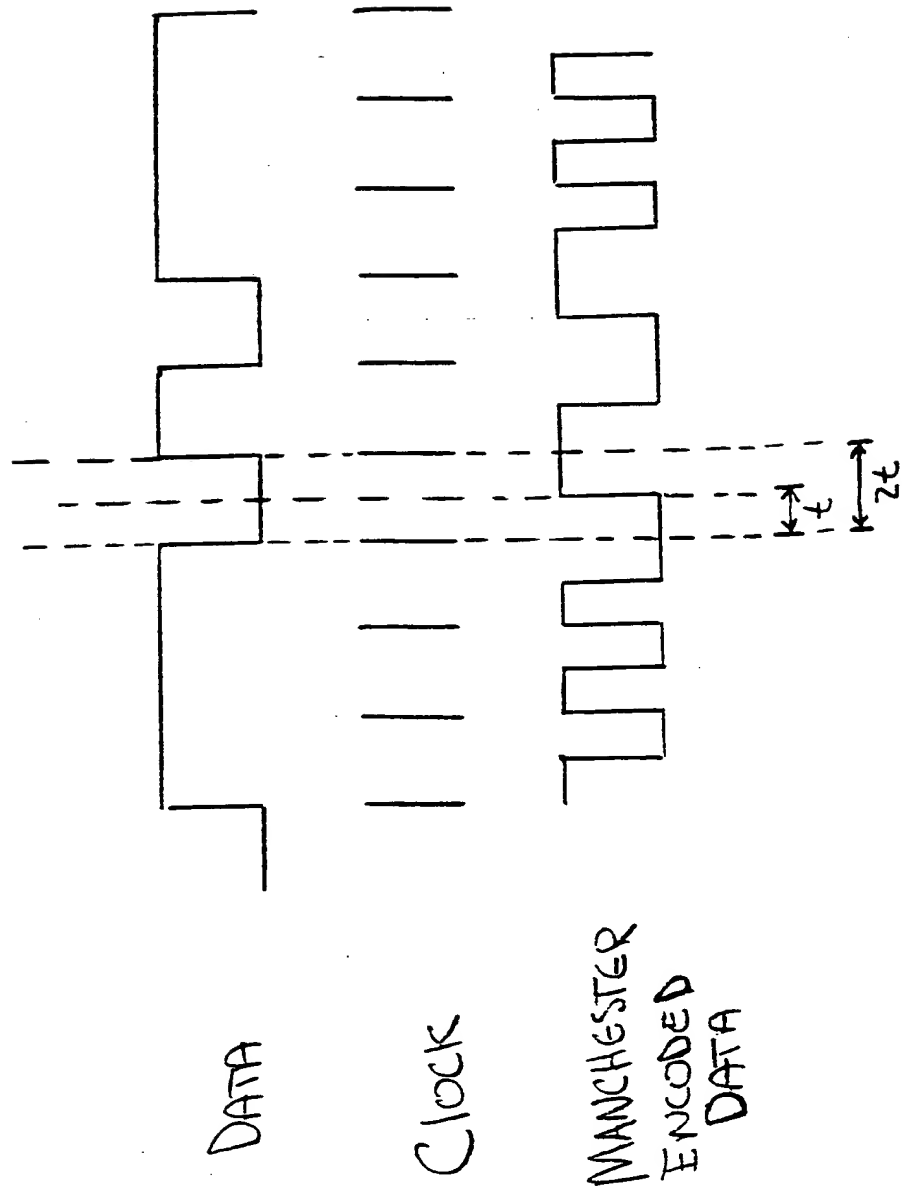


Fig. 6

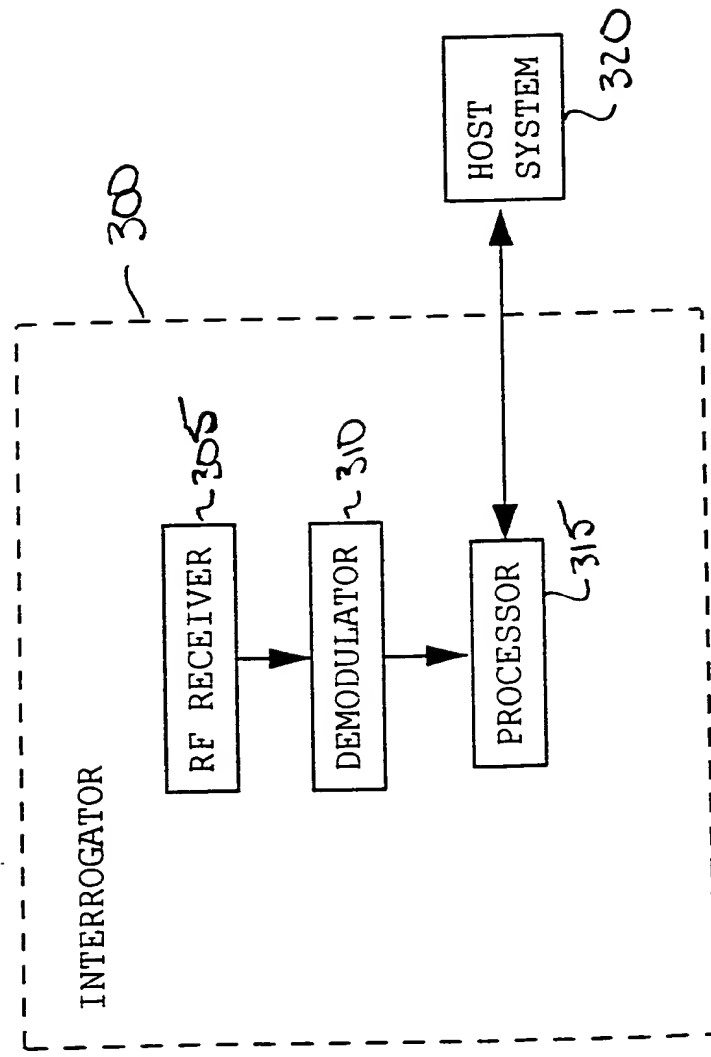


FIG. 6 is a block diagram of an interrogator system in accordance with the present invention.



Fig. 7

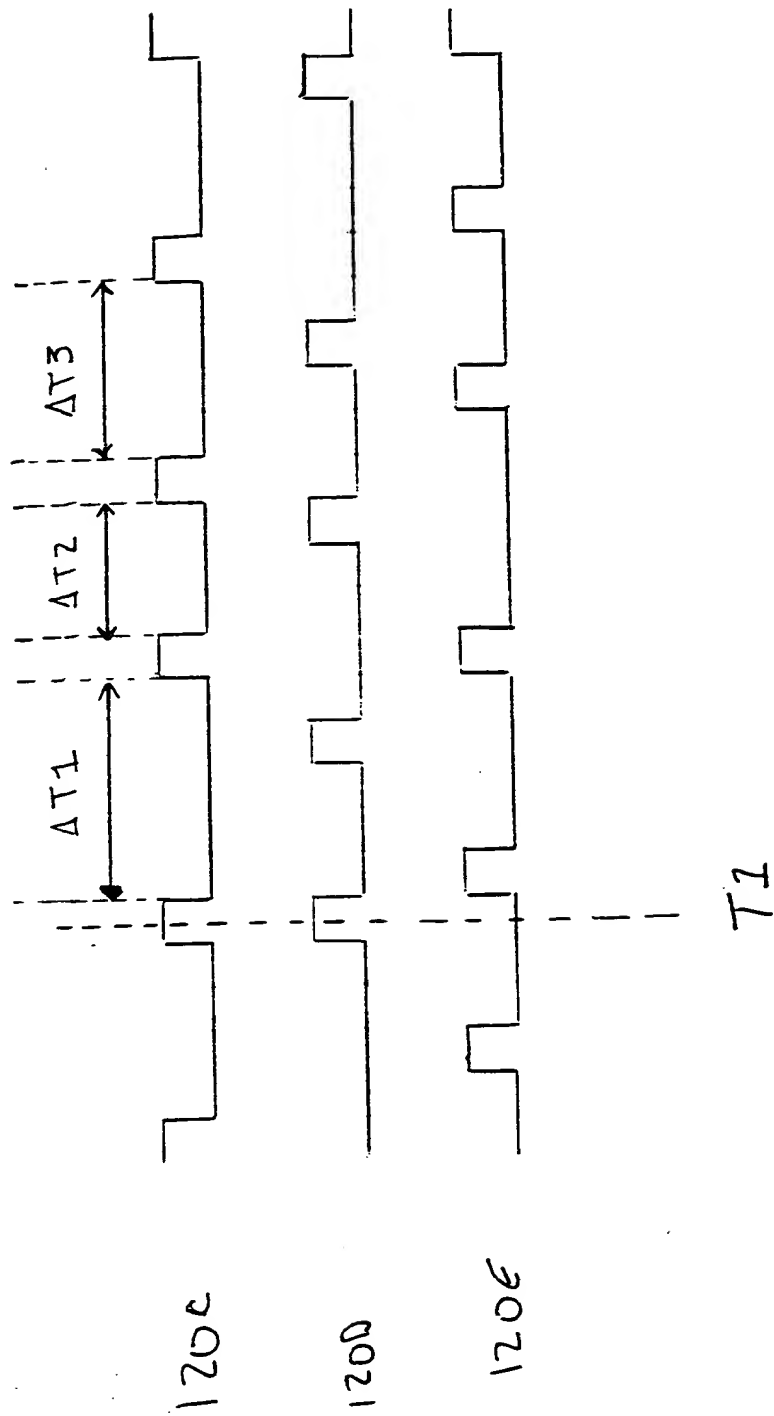


Fig. 8



51 52 53 54 55 56 57 58 59 510 511 512 513 514 515 516

Fig. 9

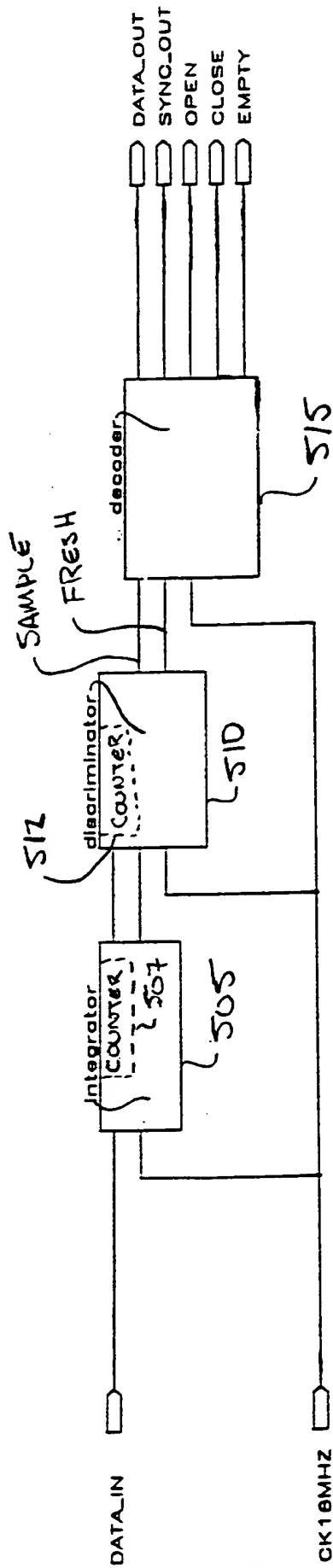


Fig. 10

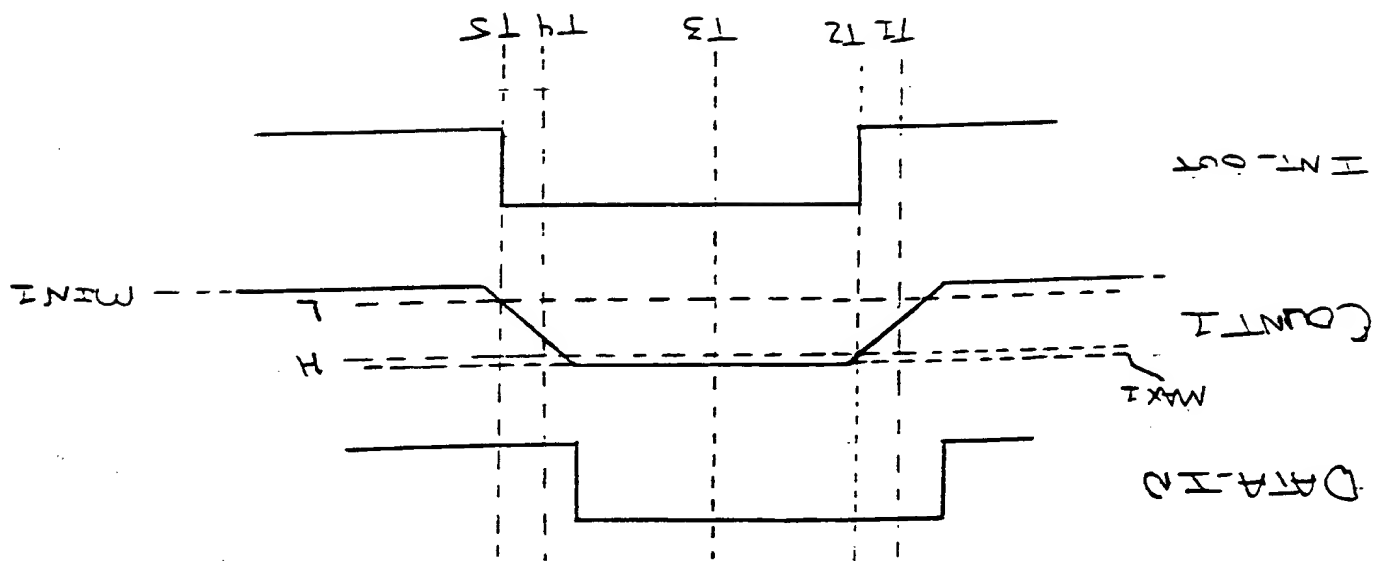


Fig. 11

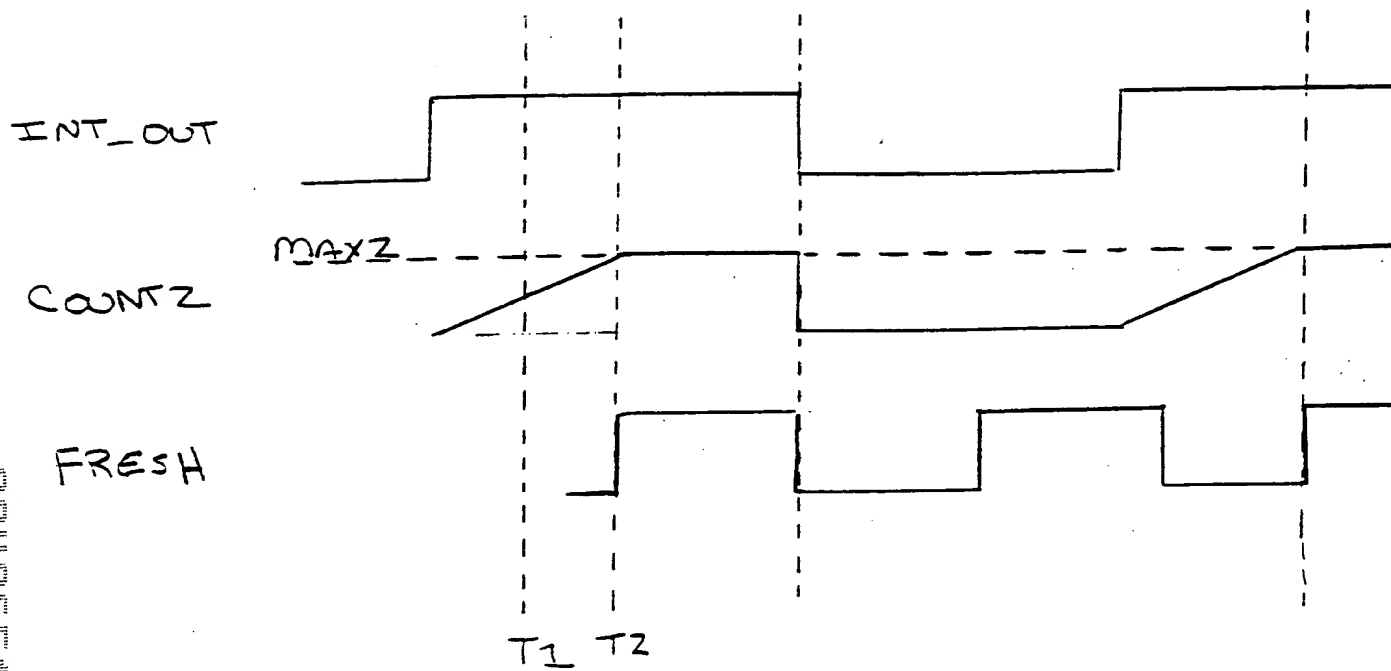


Fig. 12

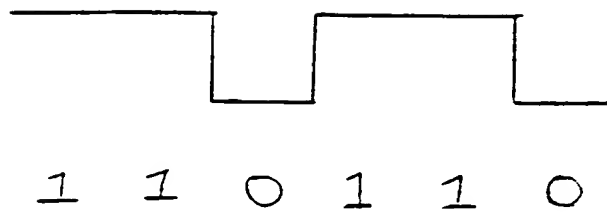


Fig. 13

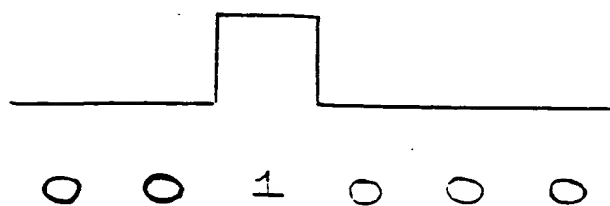
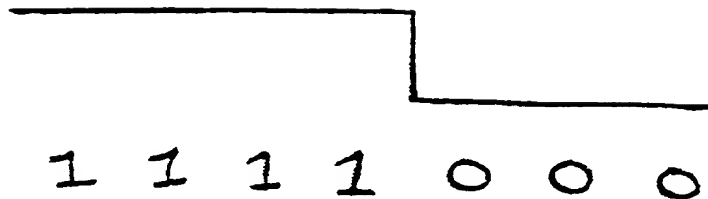


Fig. 14

Prior Art



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